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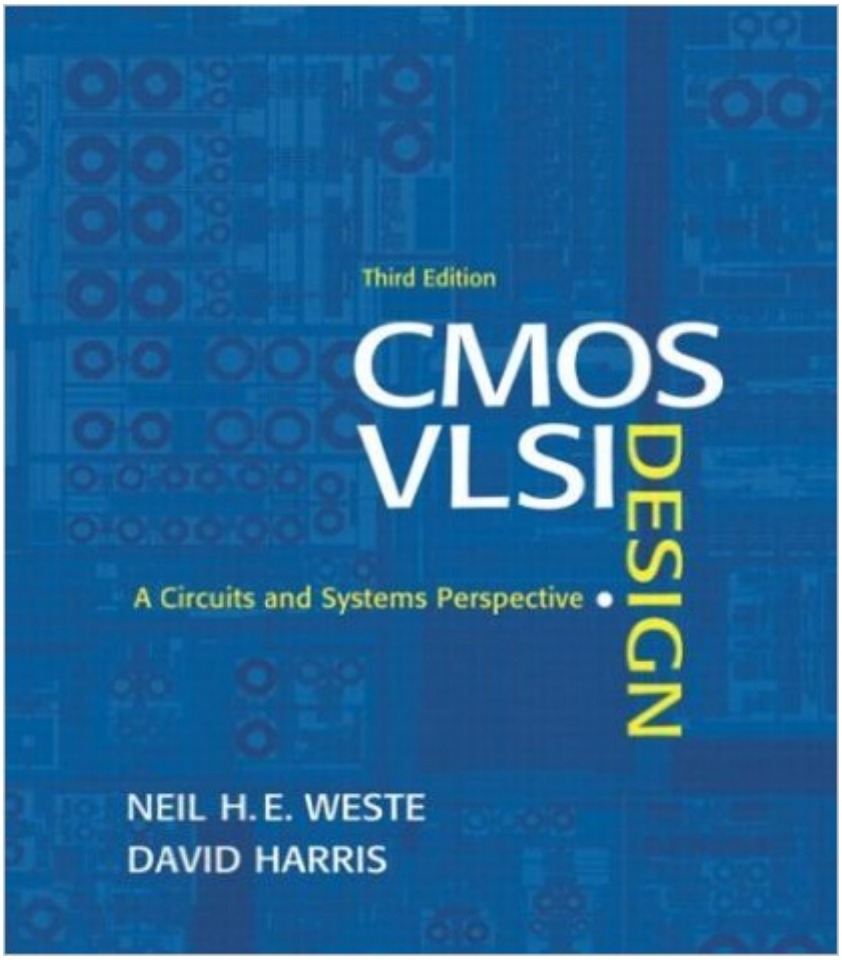
CMOS VLSI Design: A Circuits And Systems Perspective (3rd Edition)



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Synopsis

The extensively revised 3rd edition of CMOS VLSI Design details modern techniques for the design of complex and high performance CMOS Systems-on-Chip. The authors draw upon extensive industry and classroom experience to explain modern practices of chip design. The introductory chapter covers transistor operation, CMOS gate design, fabrication, and layout at a level accessible to anyone with an elementary knowledge of digital electornics. Later chapters beuild up an in-depth discussion of the design of complex, high performance, low power CMOS Systems-on-Chip.

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Customer Reviews

I recently taught a senior undergraduate/first year graduate introductory course with VLSI with this book as the text. I found this book to be confusing and frustrating to the students. In order to lecture on VLSI topics in what I thought was a logical order, I had to jump around in the text book. My main complaint is that the book is organized more like an encylopedia and not like a textbook. As such I think it makes a very good reference for those with previous training or experience in the VLSI field, but confusing and unhelpful for those learning the field for the first time.Oddly, the second edition of Weste (Weste and Eshragian) is far better organized and much more coherent in its development of topics within VLSI. I found myself often going back to the second edition when I was preparing lectures.A second complaint is that the book introduces logical effort as a primary topic early on in the discussion of switching delays, in my opinion at the expense of discussion of the fundamental

circuit mechanisms in switching delay (which again are discussed in detail in Weste 2nd Ed). The emphasis on logical effort continues throughout the text. Again, a choice that is reasonable if your audience is experienced engineers but not for an introductory course.I will probably change texts for next year, most likely to the text by Rabaey et al, which appears to be much better organized for an introduction to VLSI. In sum, Weste 3rd edition might make a good text for a second or third course in VLSI, or a good reference for practitioners in the field, but not a good text for a first course in VLSI.

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